

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1339	711/141.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/02 07:22
L2	47	L1 and CMP	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/02 07:22
L3	6	L1 and ring and ((merg\$3 or coalesc\$3 or append\$3 or concatenat\$3) near5 (buffer or cache or store))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/02 07:22
L4	2824	711/118.ccls. or 711/142.ccls. or 711/121.ccls. or 711/130.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/02 07:22
L5	17	L4 and ring and ((merg\$3 or coalesc\$3 or append\$3 or concatenat\$3) near5 (buffer or cache or store))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/02 07:22
L6	24	(cache near2 coherenc\$1) and ((merg\$3 or coalesc\$3 or append\$3 or concatenat\$3) near5 (buffer or cache or store)) and (ring with (processor or cpu or core))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/02 07:22
L7	58	(ring with synchronous) and (fixed near3 (delay or latency))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/02 07:22
L8	32	(ring and synchronous).ti. and (bi-directional or bidirectional)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/02 07:23

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L9	47	numa and ring same (processor or core or cpu or host) and cache and (merg\$3 or coalesc\$3 or concatenat\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/02 07:23
L10	22	processor with ring with synchronous	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/02 07:23
S15 6	385	(numa and integrated circuit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/01 14:51
S15 7	348	(numa and integrated circuit) and cache	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/01 14:51
S15 8	45	((numa and integrated circuit) and cache) and 711/141.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/01 14:56
S15 9	1492	(module with single integrated circuit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/01 14:56
S16 0	156	(module adj2 single integrated circuit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/01 14:57

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S16 2	10	(module adj2 single integrated circuit) and 711/???.ccis.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/01 14:57
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